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Title of Invention	OPTIMIZED CONDUCTIVE LID MOUNTING FOR INTEGRATED CIRCUIT CHIP CARRIERS	
Application Number :		
Date :		
First Named Applicant:	Michael A. Gaynes	
Confirmation Number:		
Attorney Docket Number:	FR920030002US1	
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Documents being submitted:	Files
us-assignment	FR920030002US1-usassn.xml us-assignment.xsl us-assignment.dtd FR920030002US1Assign1.tif FR920030002US1Assign2.tif
us-request	FR920030002US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FR920030002US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FR920030002US1Dec2.tif
us-declaration	FR920030002US1Dec.tif
application-body	FR920030002US1spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd fr920030002us1drawing1.tif fr920030002us1drawing2.tif fr920030002us1drawing3.tif fr920030002us1drawing4.tif
Comments	
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